

Fig 1

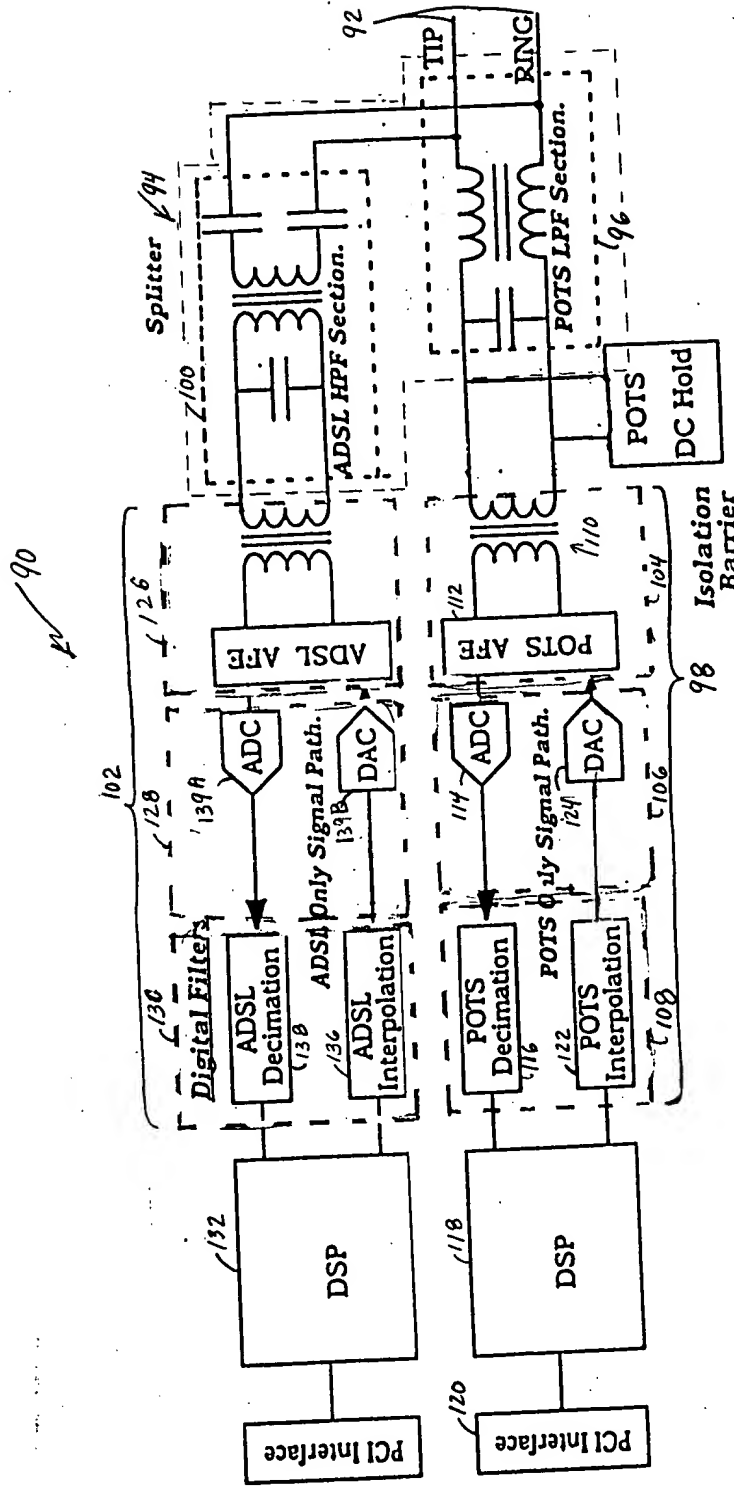


Fig. 3 Prior Art

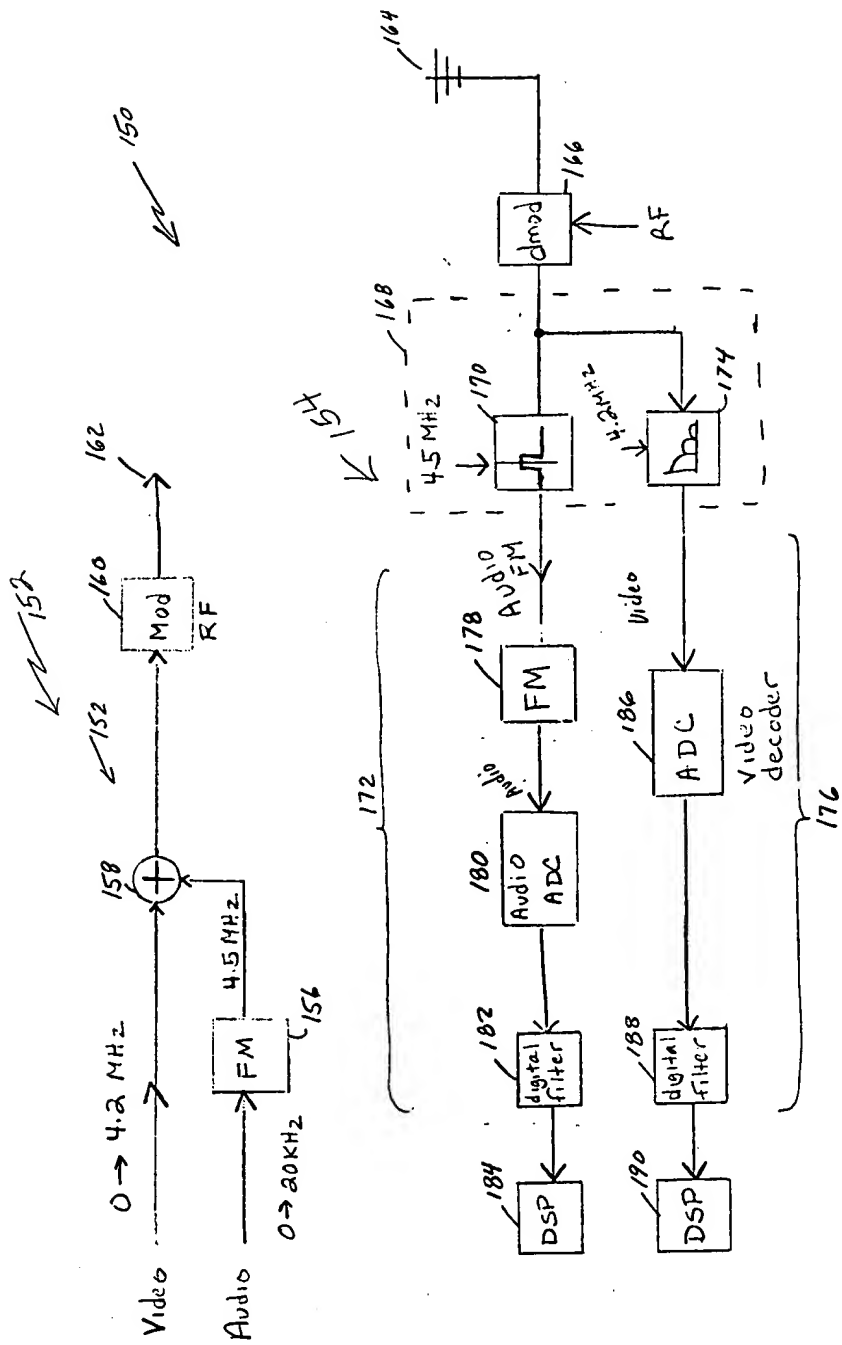


Fig 4 Prior Art

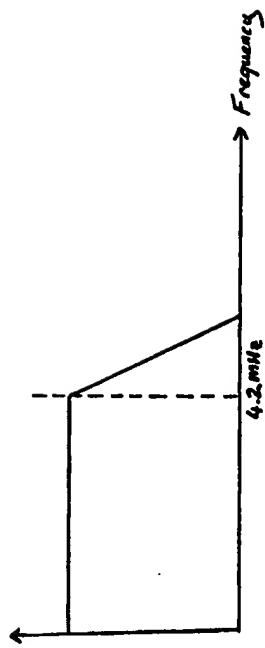


Fig. 5 Prior Art

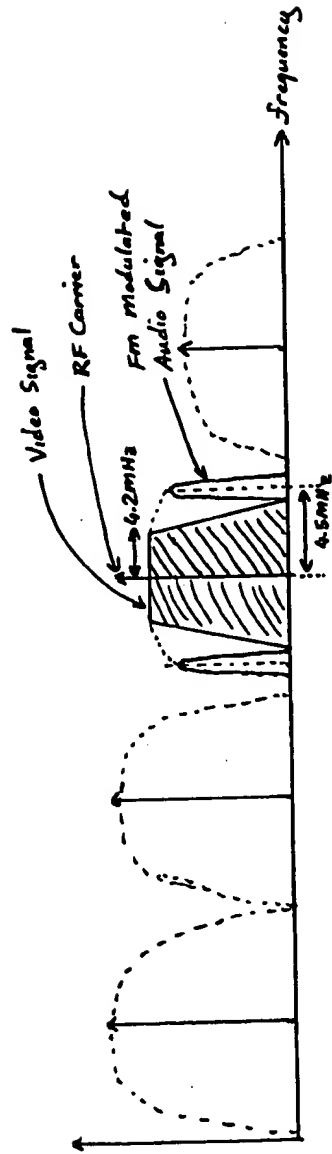


Fig. 6 Prior Art

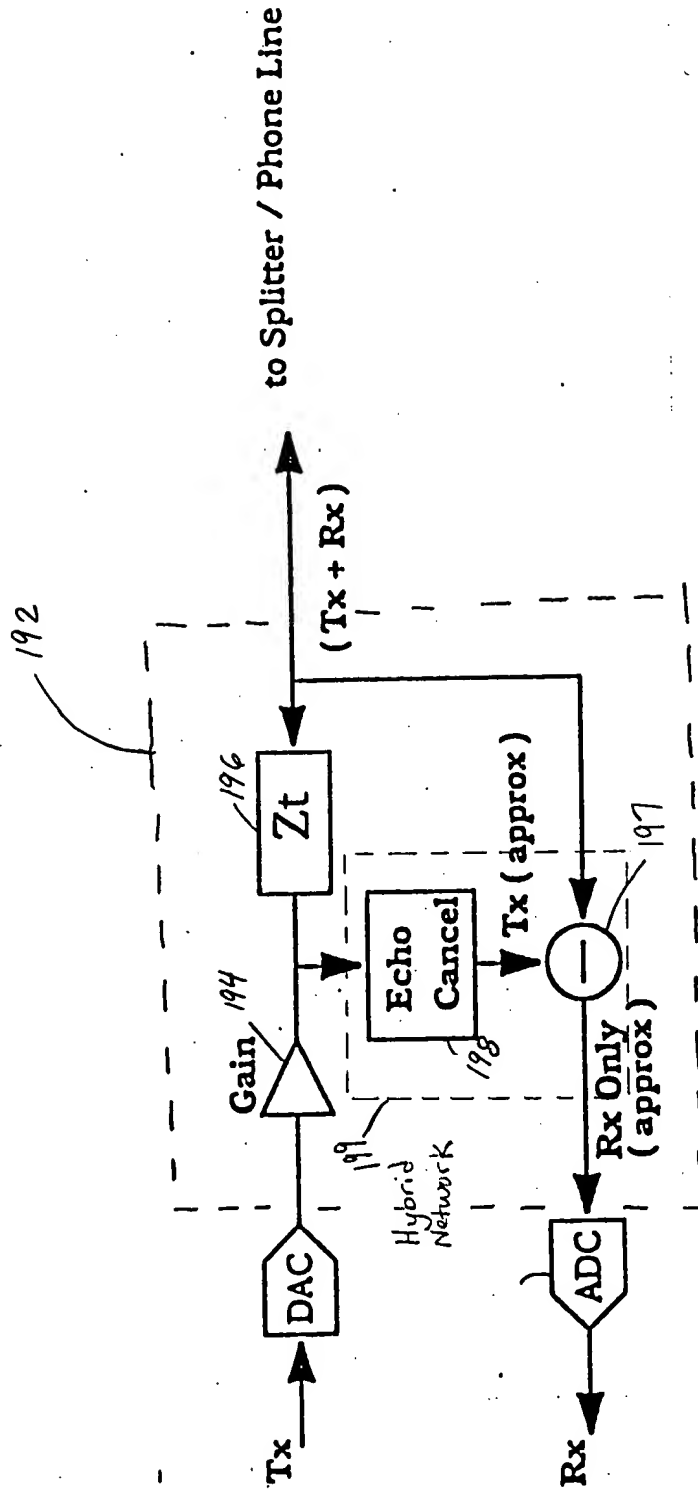


Fig. 7 Prior Art

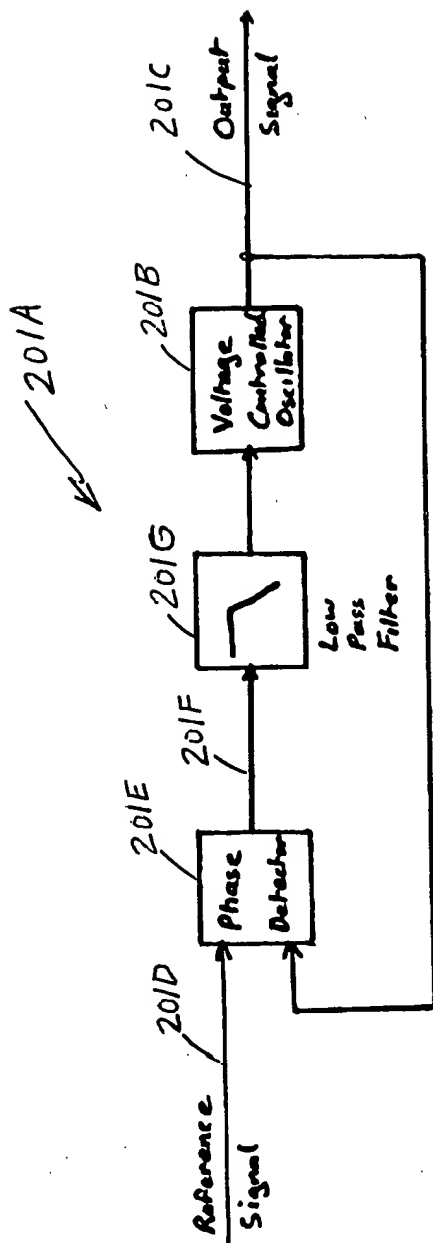


Fig 8 Prior Art

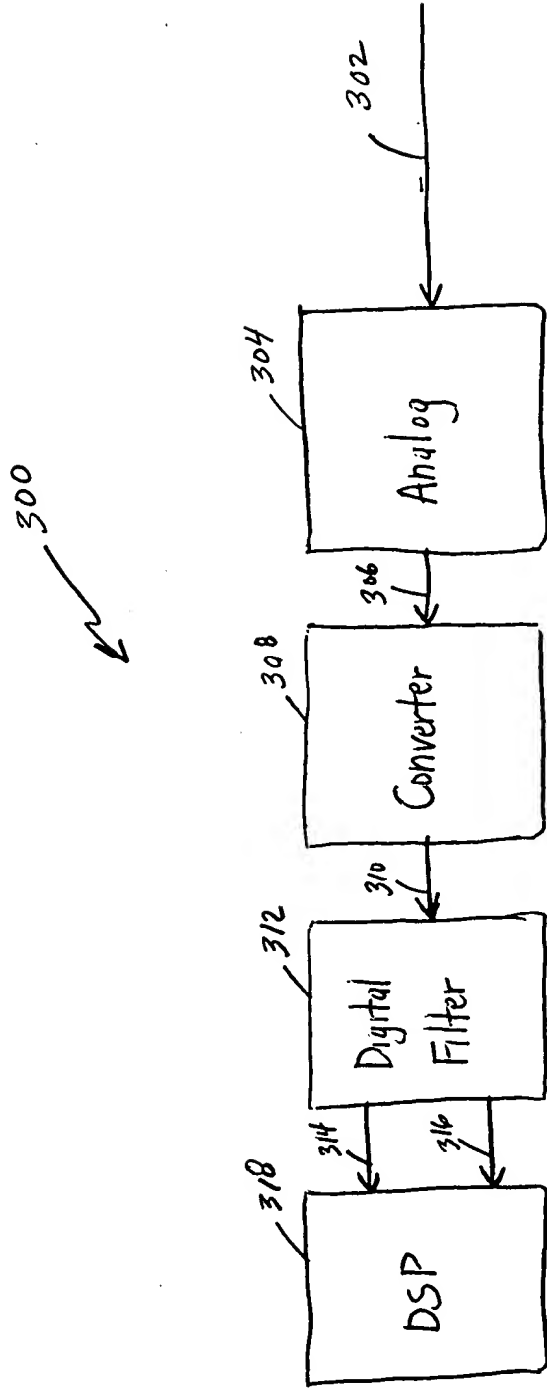


Fig. 10

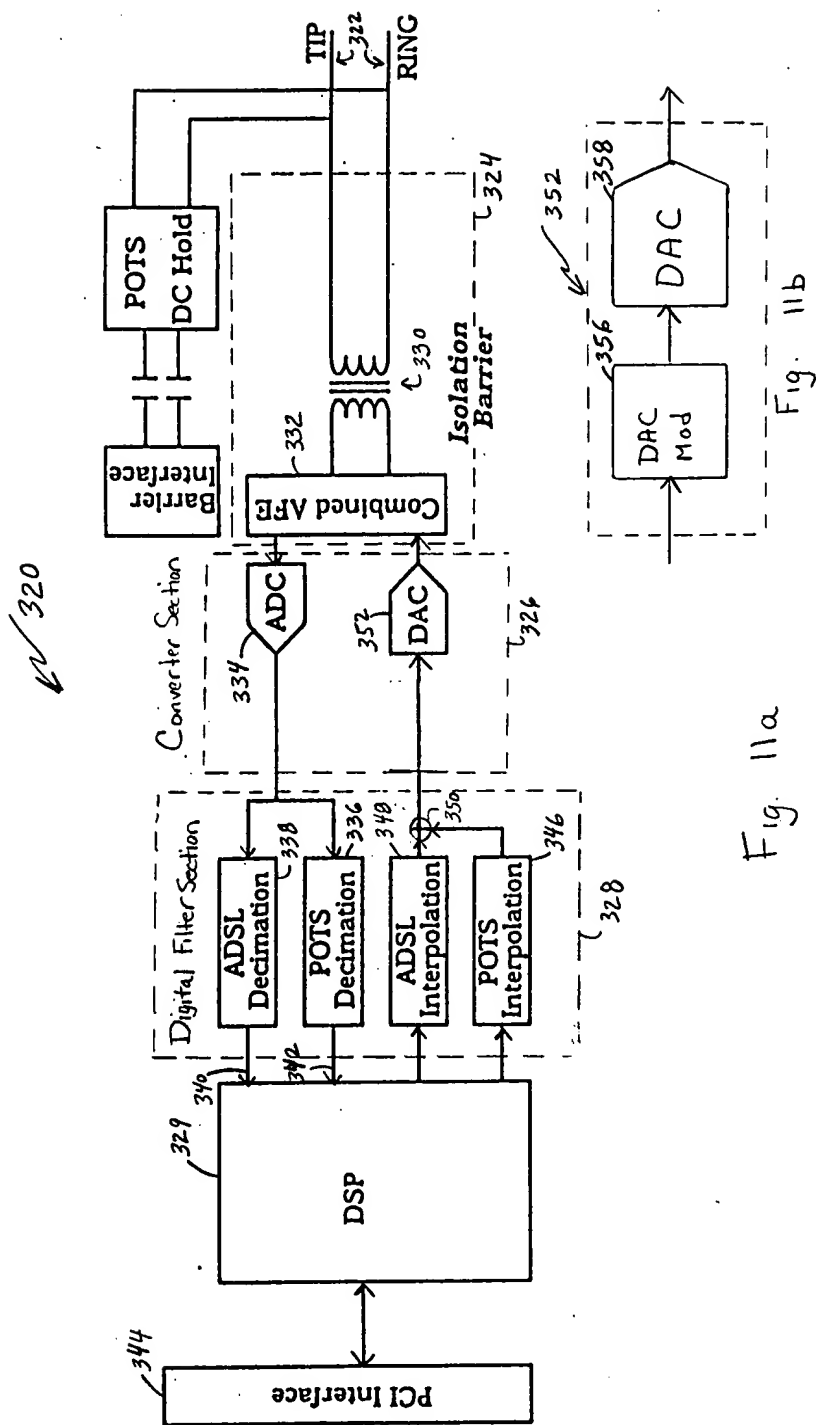


Fig. 11a

Fig. 11b

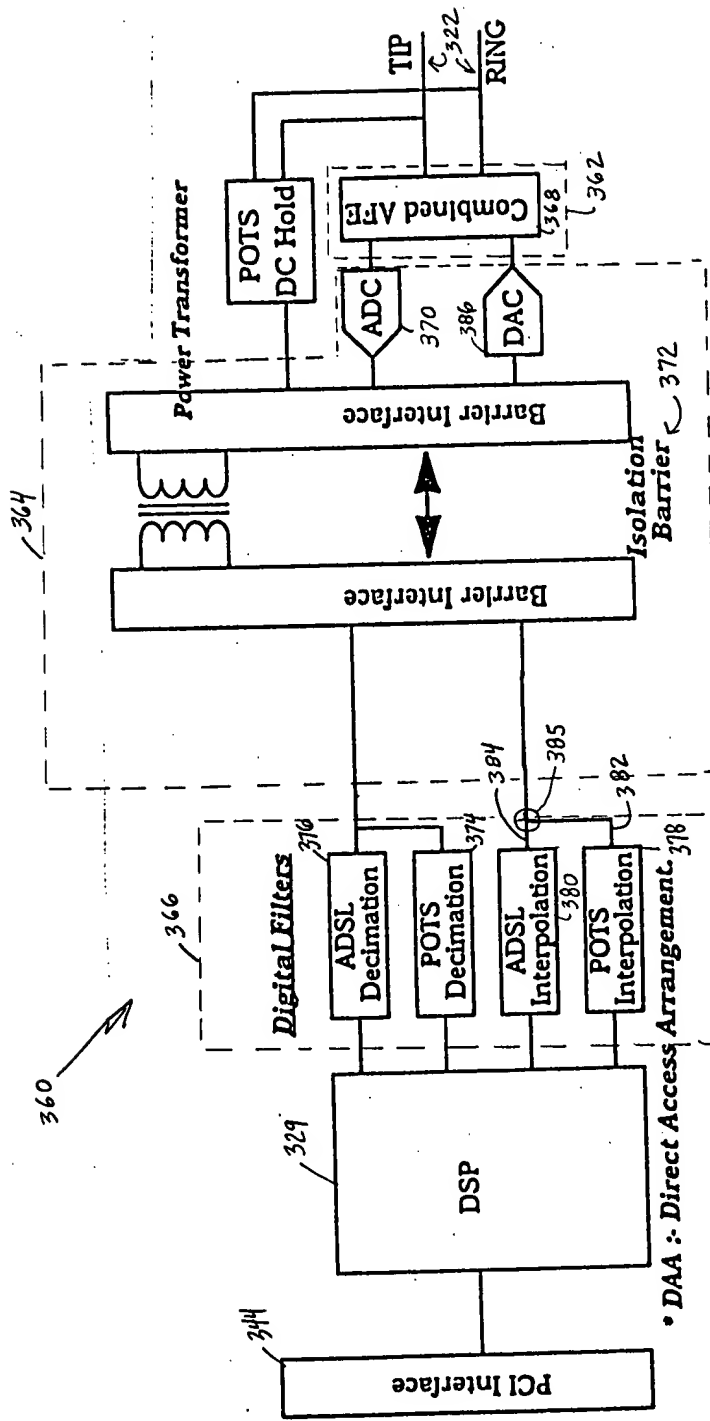


Fig. 12a

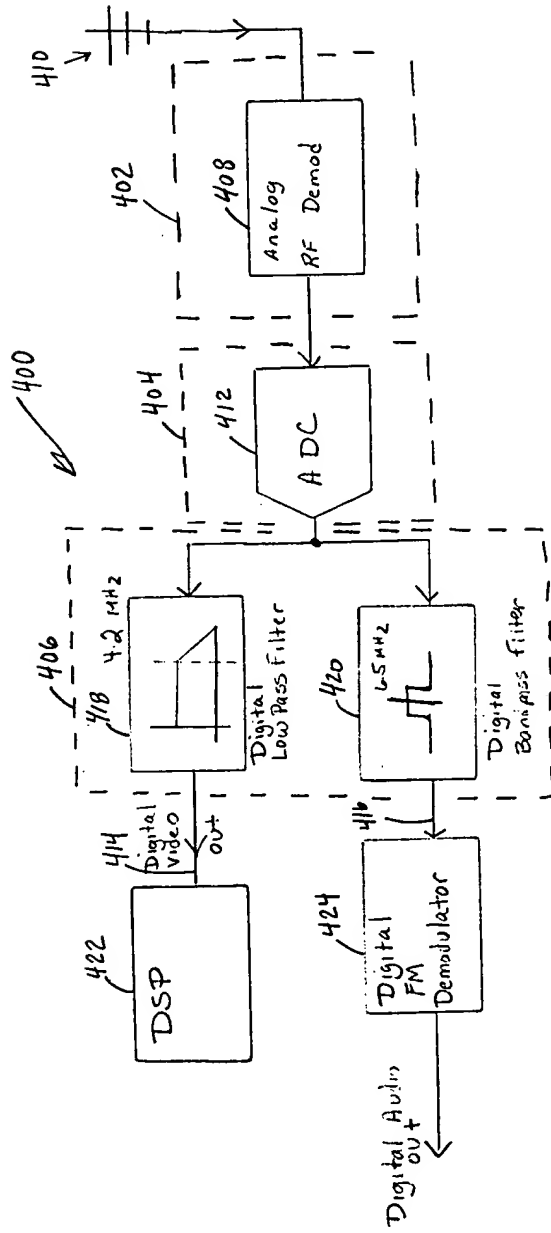


Fig. 13

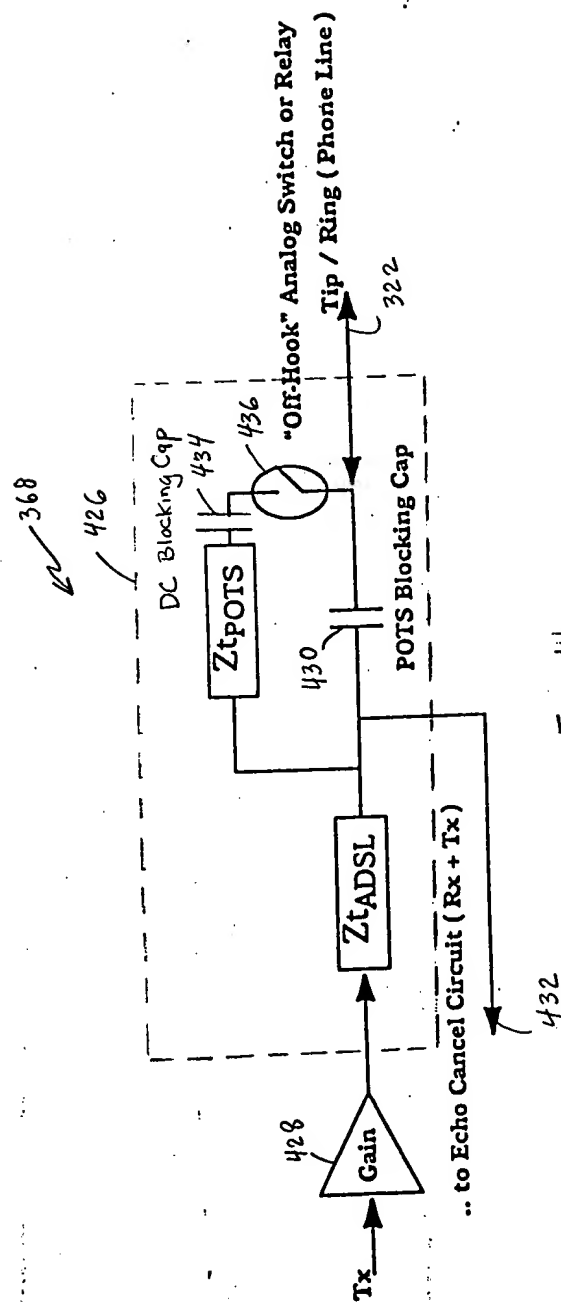


Fig. 14a

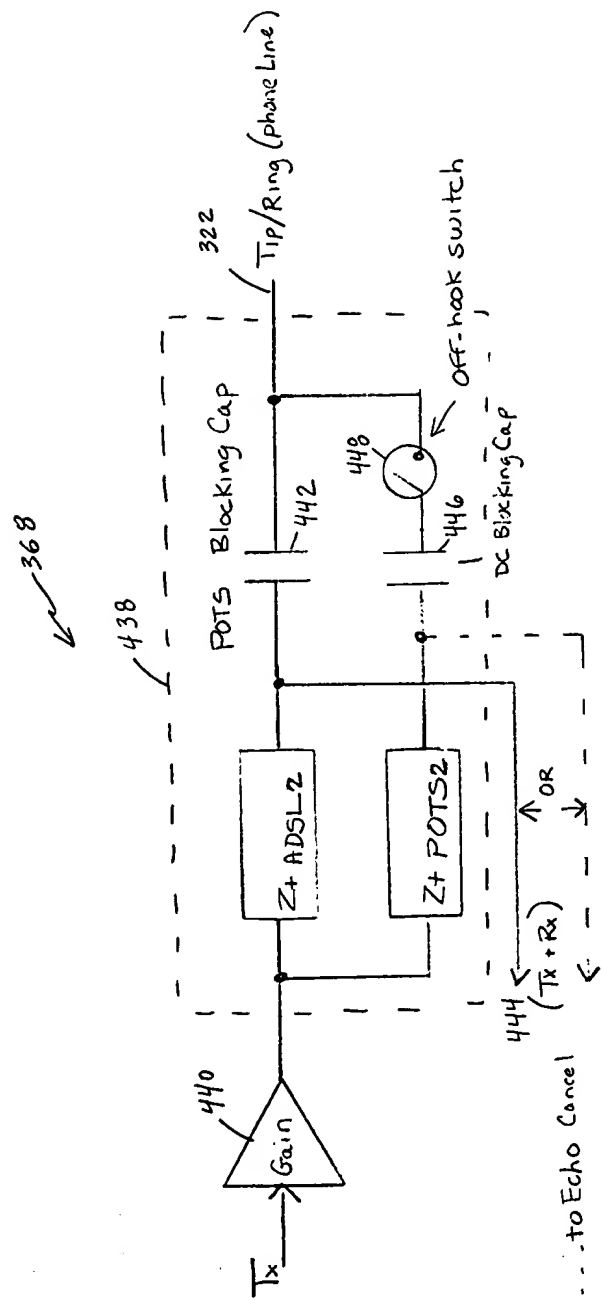


Fig 14b

ADSL + POTS

1000Ω

frequency

100 Hz

4 kHz

25 kHz

approximately 1.2 KΩ

approximately 700 Ω

442

Fig. 15b

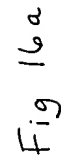


Fig 16a

U.S. Pat. No. 6,811,111 B2

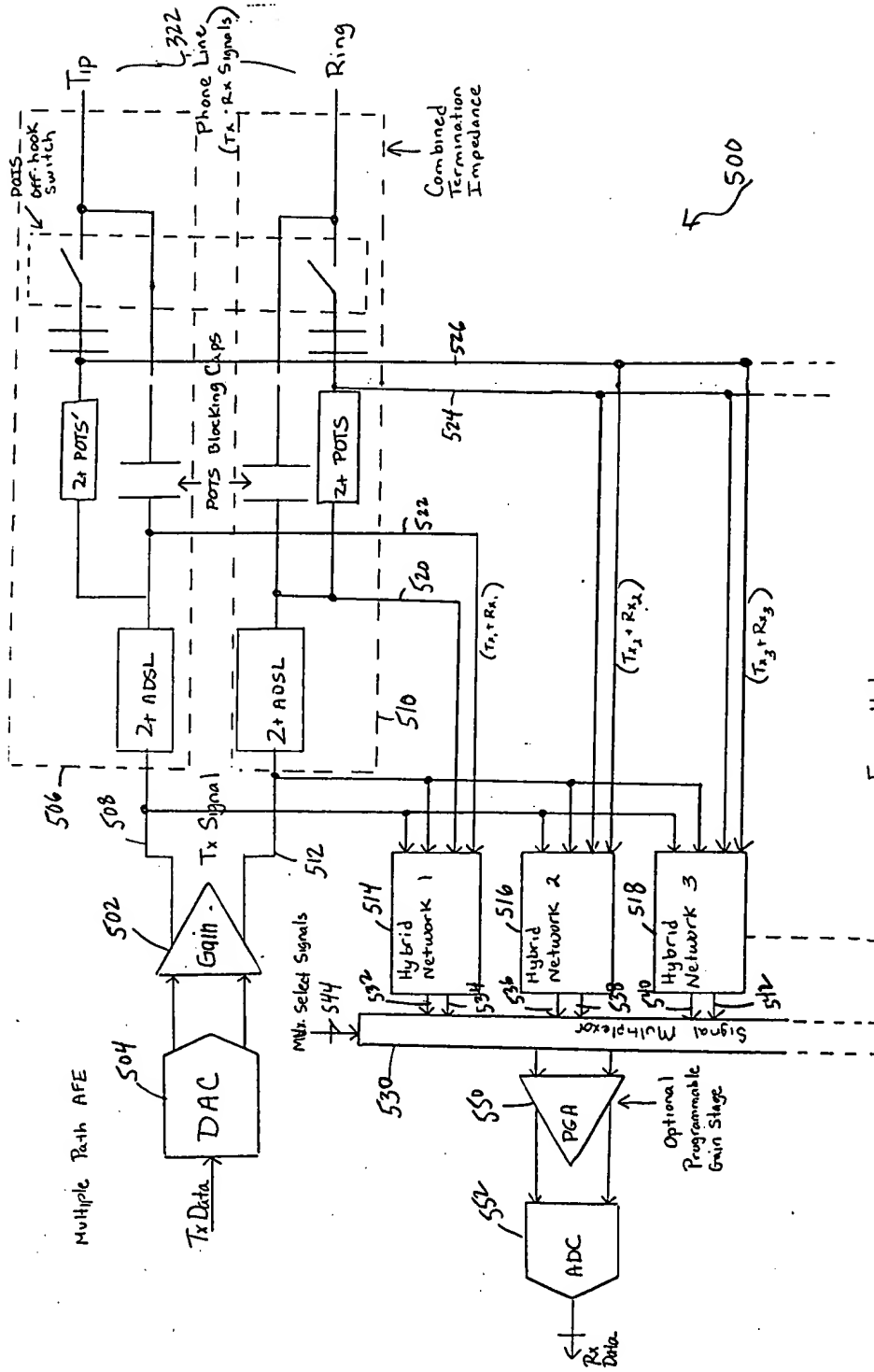


Fig 16b

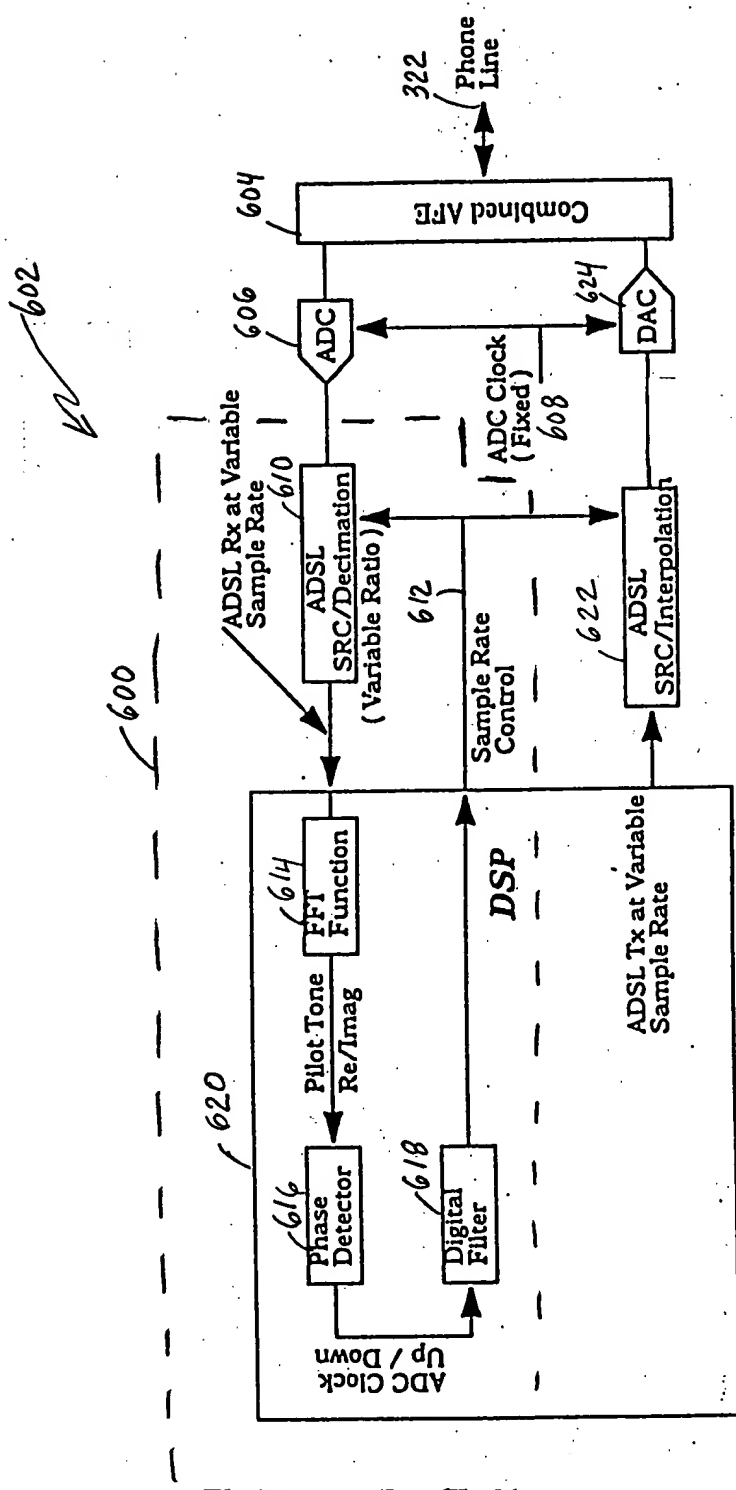
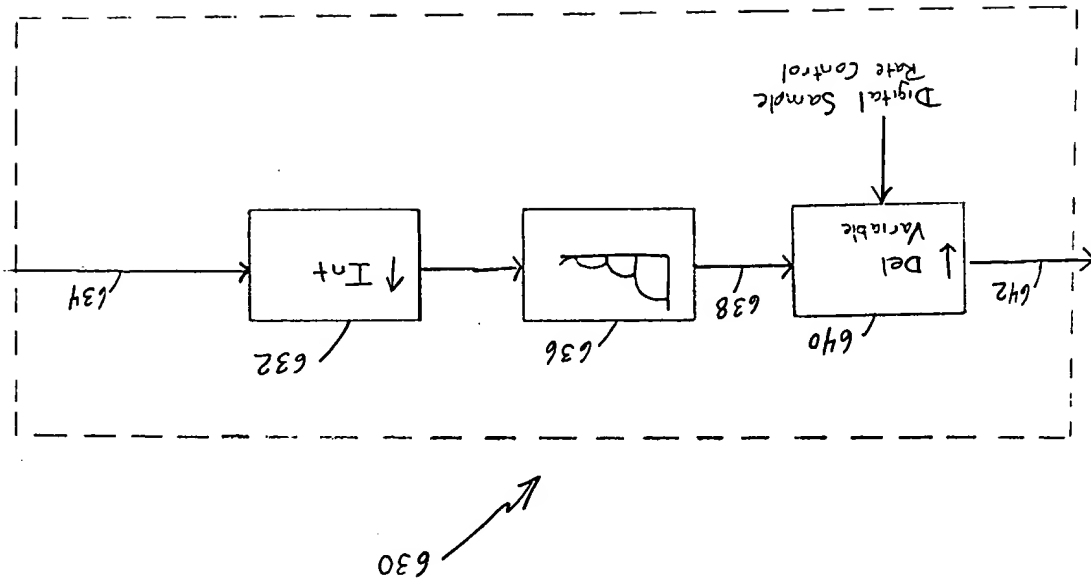


Fig. 17

Fig 18



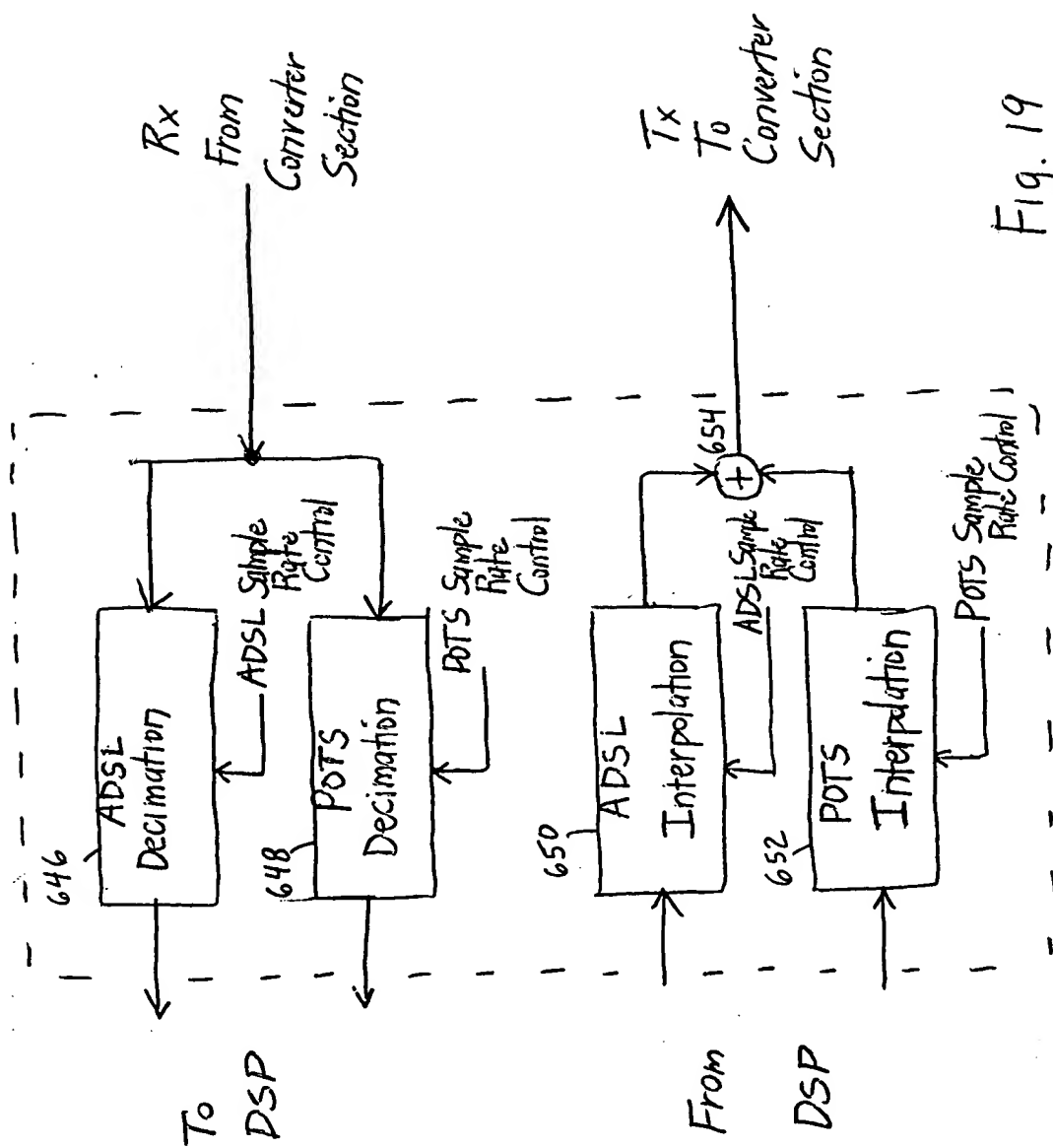


Fig. 19

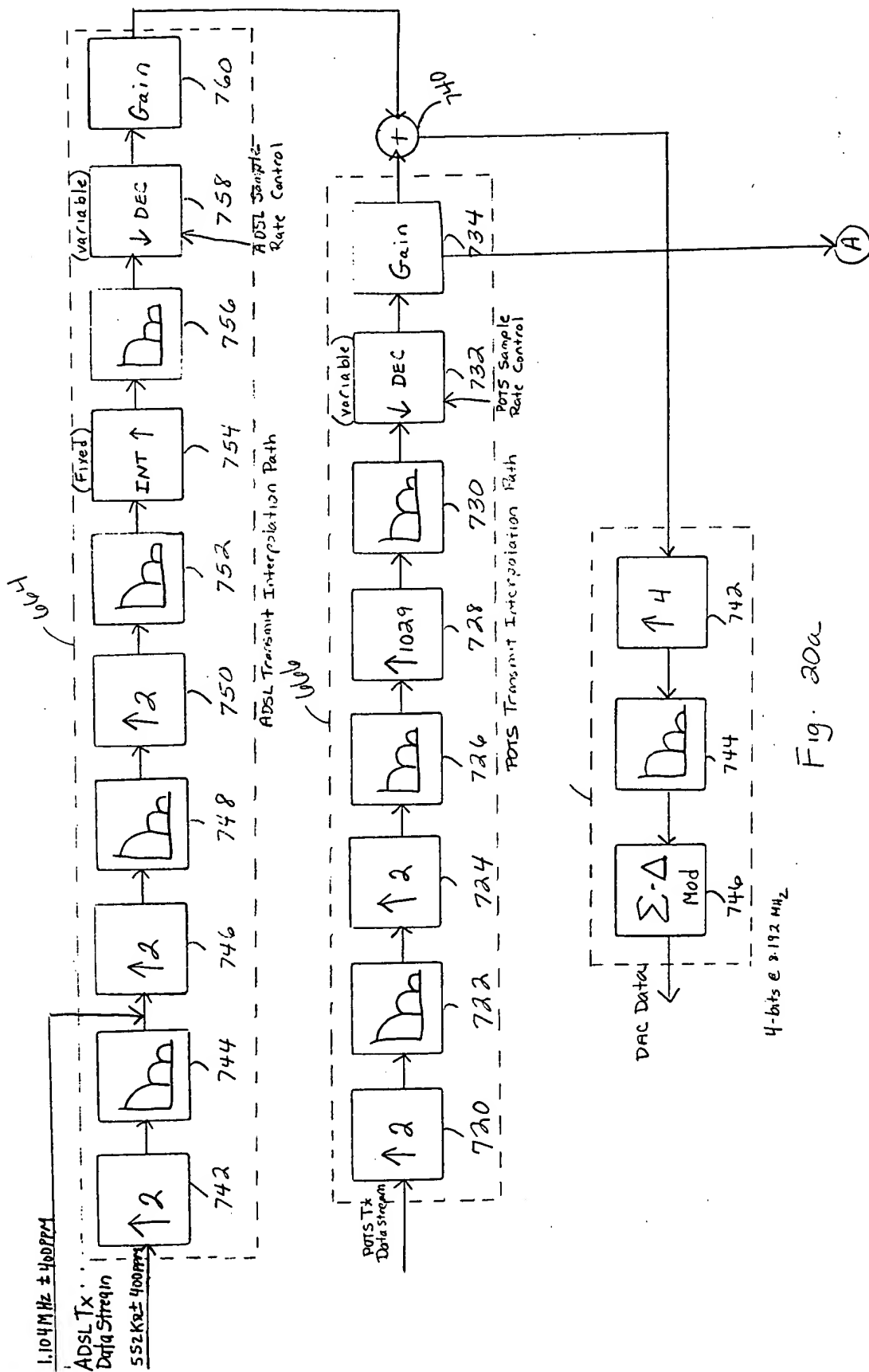


Fig. 20a

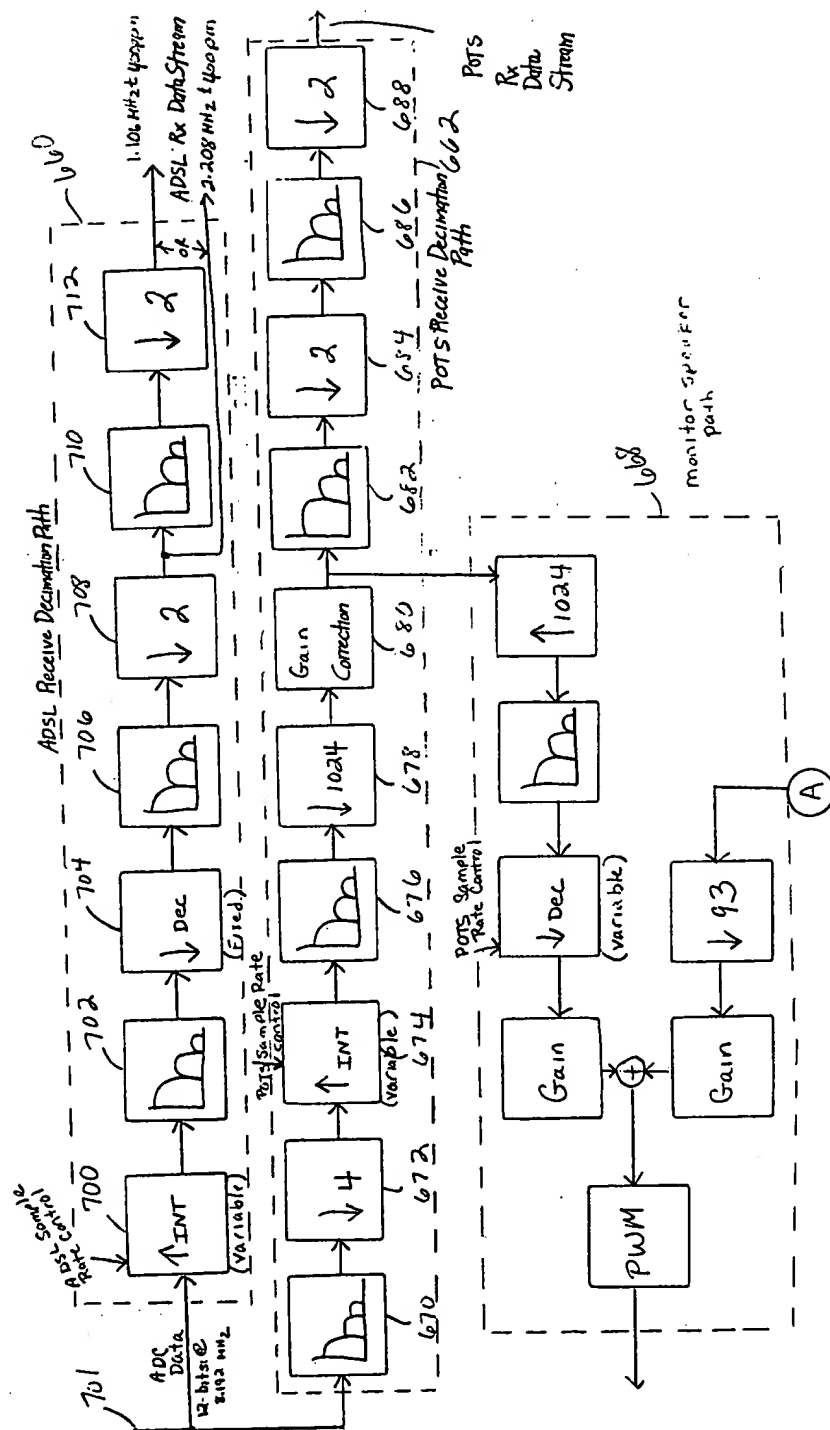


Fig. 20b

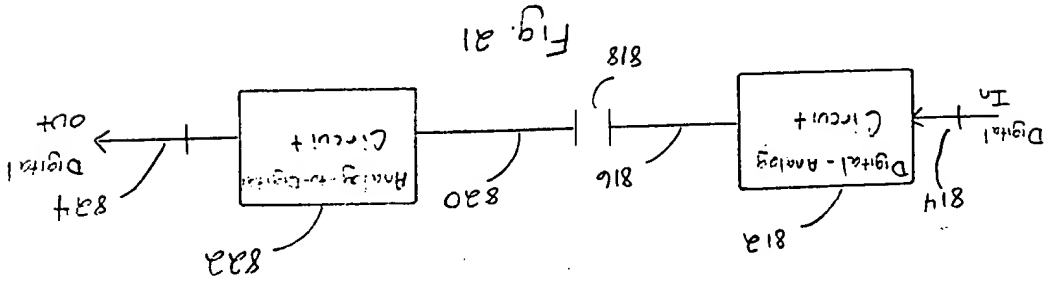


Fig. 21

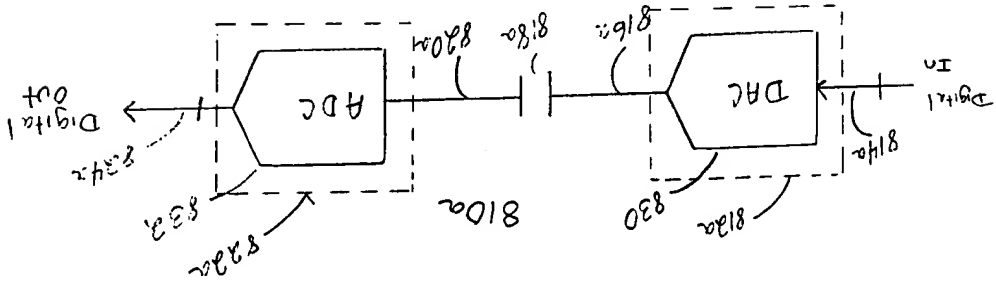


Fig. 22

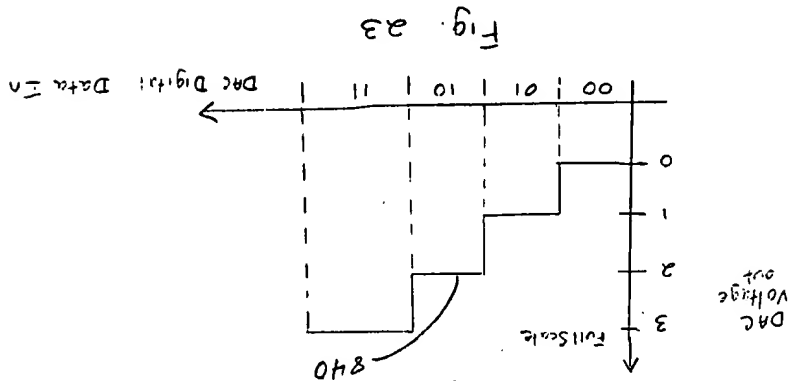


Fig. 23

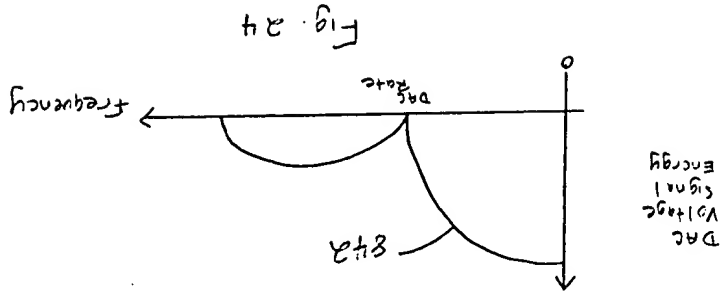


Fig. 24

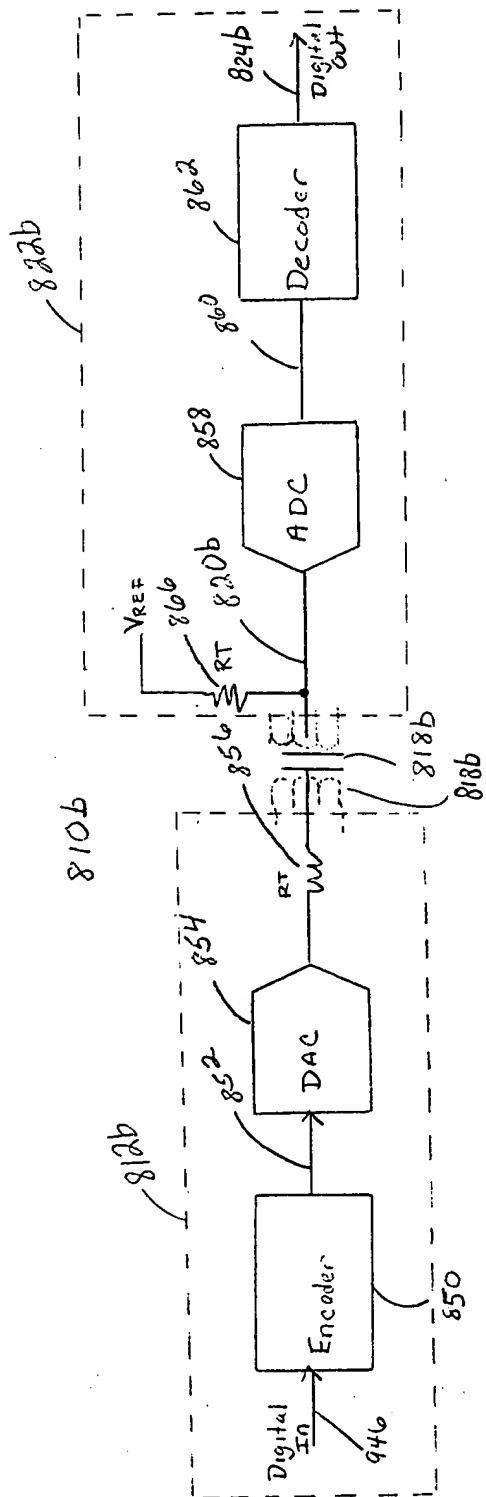


Fig. 25

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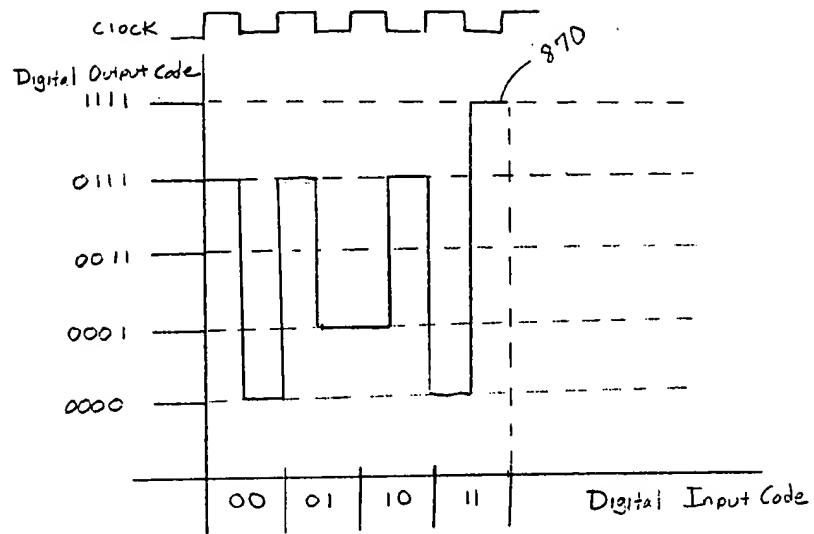


Fig. 26

DAC

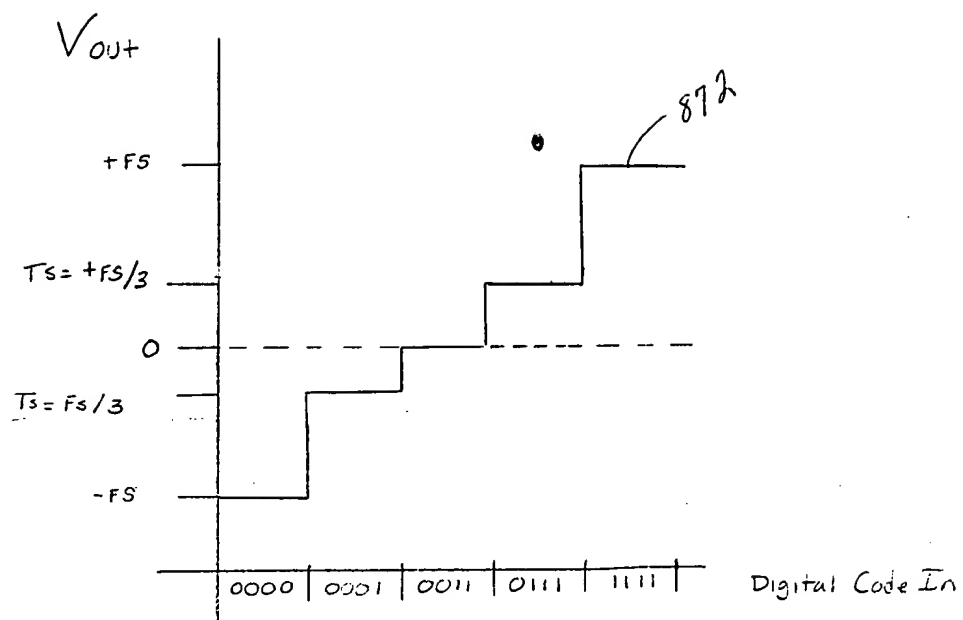


Fig. 27

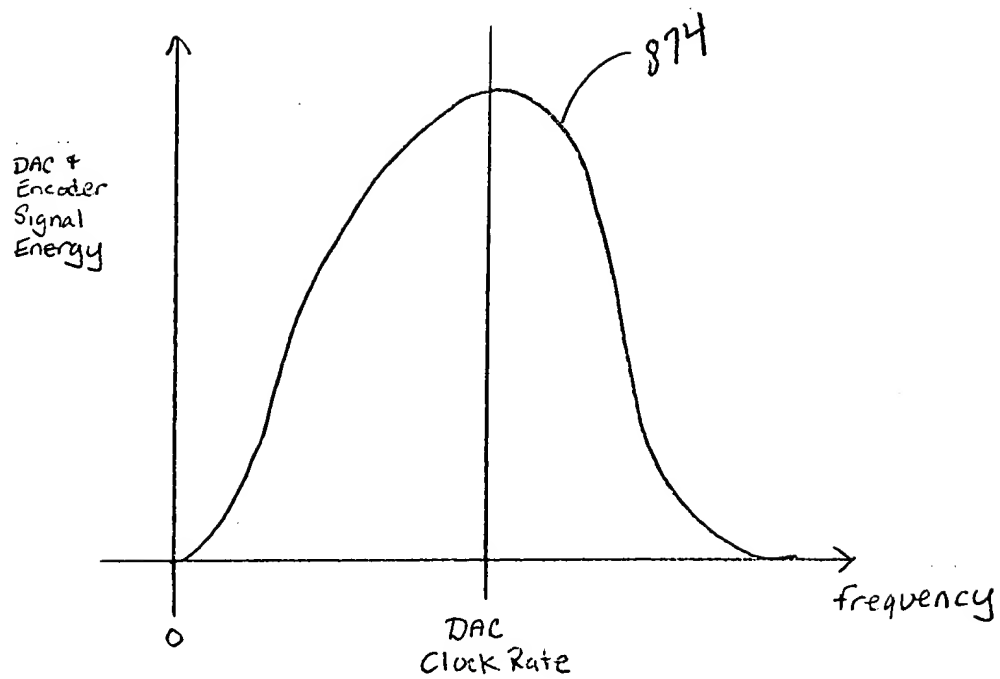


Fig. 28

75
11

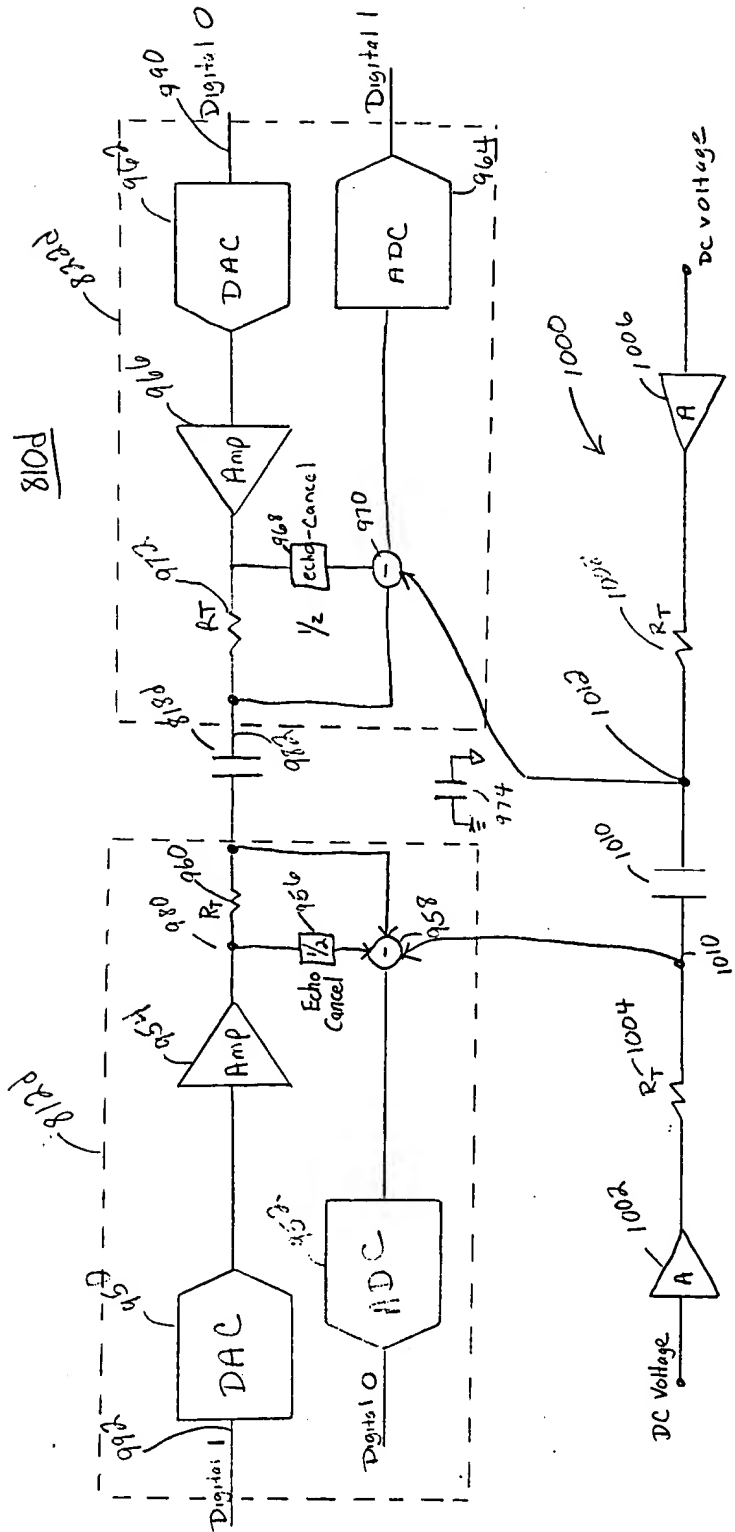


Fig 30

Full Barrier System

810e

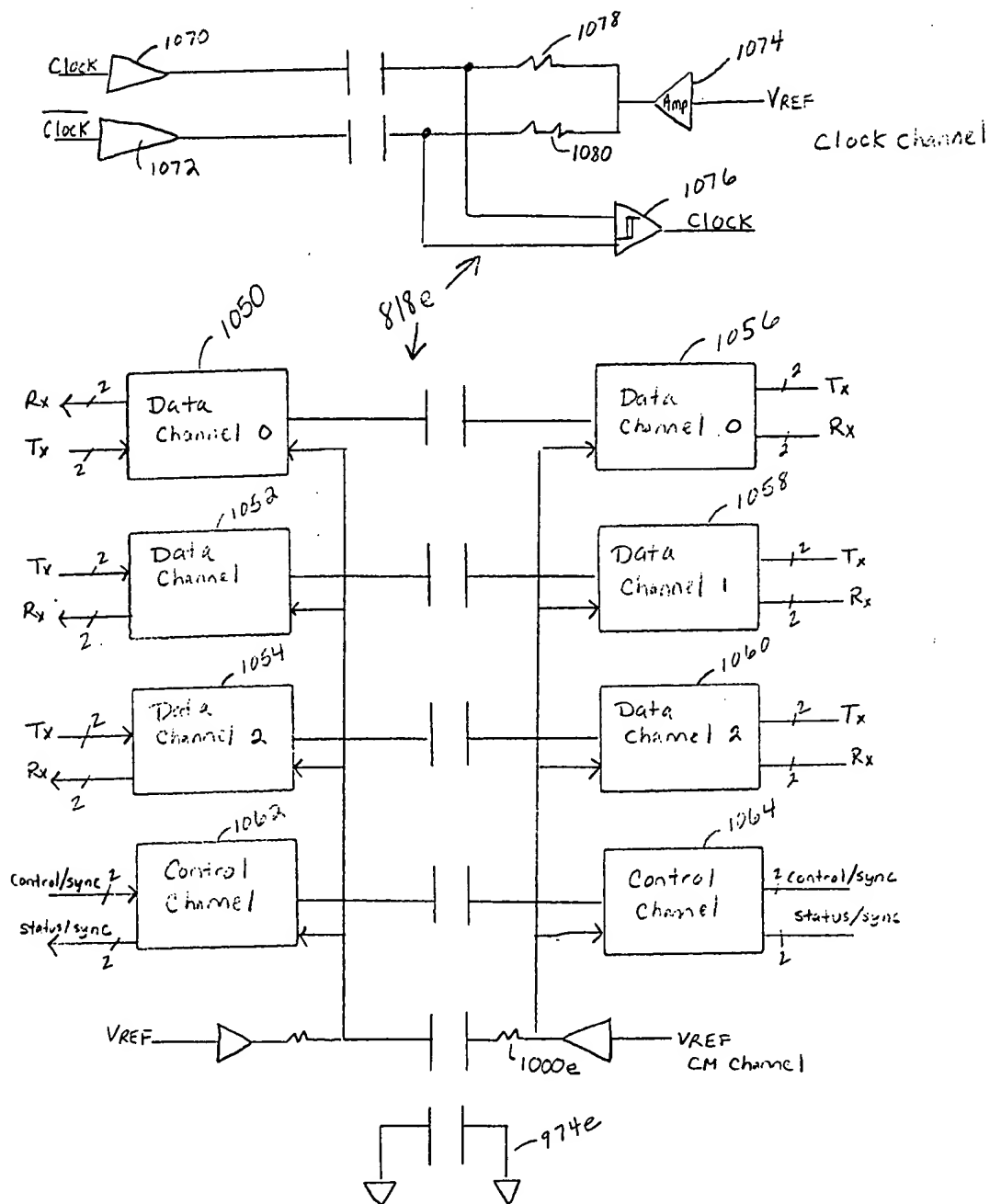


Fig. 31

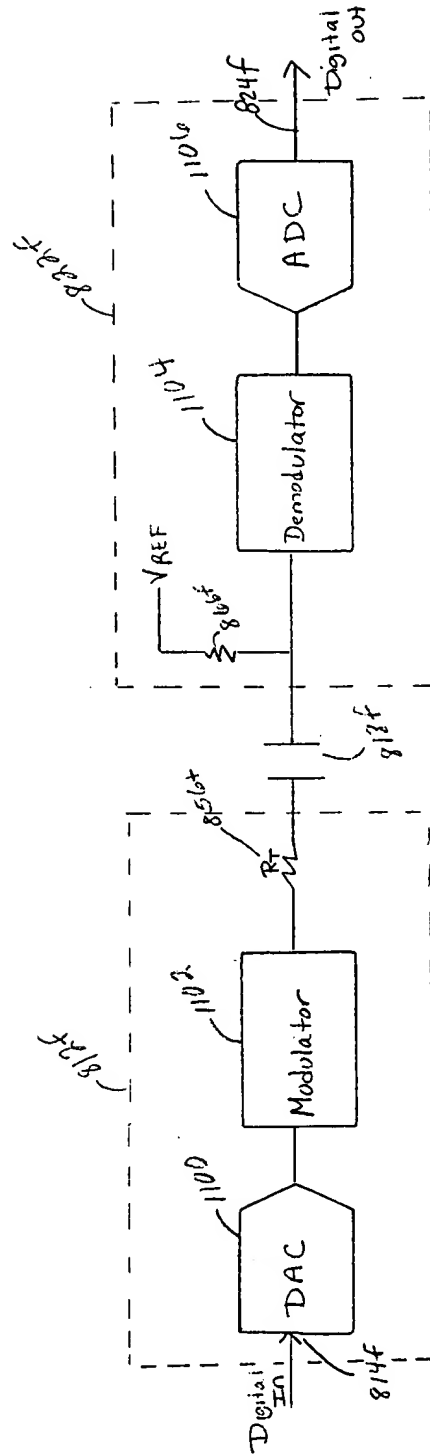


Fig. 32